What Is Claimed Is:

1. A method of fabricating a liquid crystal display device, comprising:

forming a first testing terminal along a side surface of a first substrate;

forming a second testing terminal along a side surface of a second substrate;

attaching the first and second substrates together to expose the first and second testing terminals; and

conducting a lighting test using the first and second testing terminals.

2. The method according to claim 1, further comprising a step of conducting an eye test for detecting defects of the liquid crystal display device by disposing a first polarizer at a rear surface of one of the first and second substrates and a second polarizer at a rear surface of another of the first and second substrates, and rotating one of first and second the polarizers.

3. The method according to claim 1, further comprising a step of dispensing liquid crystal on one of the first and second substrates.

4. The method according to claim 1, further comprising a step of forming a plurality of individual liquid crystal panels by cutting the first and second substrates after conducting the lighting test.

5. The method according to claim 1, wherein preparing the first substrate comprises:

providing a transparent substrate;

forming a color filter on the transparent substrate; and

forming a common electrode on the color filter.

6. The method according to claim 5, wherein the steps of forming a common electrode and forming a first testing terminal are performed simultaneously.

7. The method according to claim 5, wherein the common electrode is formed to be electrically connected to the first testing terminal.

8. The method according to claim 1, wherein providing the second substrate comprises:

providing a transparent substrate on which pixel areas are defined;

forming gate lines and a gate shorting bar for interconnecting the gate lines on the transparent substrate;

forming data lines insulated from and crossing the gate lines and a data shorting bar for interconnecting the data lines; and

forming a pixel electrode on the pixel area.

9. The method according to claim 8, wherein the step of forming a gate shorting bar includes forming a gate testing terminal connecting the gate shorting bar.

10. The method according to claim 9, wherein the gate testing terminal is connected to one of the first and second testing terminals.

11. The method according to claim 8, wherein the step of forming a data shorting bar includes forming a data testing terminal connecting the data shorting bar.

12. The method according to claim 9, wherein the data testing terminal is connected to one of the first and second testing terminals.

13. A method of fabricating a liquid crystal display (LCD) device, comprising:

providing a first substrate upon which a plurality of color filter substrates and a first testing terminal are formed;

providing a second substrate upon which a plurality of thin film transistor array substrates corresponding to the color filter substrates are formed, the second substrate includes a second testing terminal;

applying a sealing material along outer portions of the color filter substrates on the first substrate;

dispensing liquid crystal onto the second substrate;

attaching the first and second substrates together so that the first and second testing terminals are exposed;

conducting a first defect test of the first and second substrates by supplying voltages to the first and second testing terminals; and

dividing the attached first and second substrates into a plurality of individual liquid crystal panels.

14. The method according to claim 13, further comprising a step of conducting a second defect test by disposing a first polarizer at a rear surface of one of the first and second substrates and a second polarizer at a rear surface of another of the first and second substrates, and rotating one of first and second the polarizers.

15. The method according to claim 13, wherein the first substrate includes a color filter and a common electrode on the color filter.

16. The method according to claim 15, wherein the a common electrode and the first testing terminal are formed simultaneously.

17. The method according to claim 15, wherein the common electrode is electrically connected to the first testing terminal.

18. The method according to claim 13, wherein the second substrate includes a a gate shorting bar for interconnecting a plurality of gate lines, and a data shorting bar for interconnecting a plurality of data lines.

19. The method according to claim 18, wherein the gate shorting bar includes a gate testing terminal connecting the gate shorting bar.

20. The method according to claim 19, wherein the gate testing terminal is connected to one of the first and second testing terminals.

21. The method according to claim 18, wherein the data shorting bar includes a data testing terminal connecting the data shorting bar.

22. The method according to claim 21, wherein the data testing terminal is connected to one of the first and second testing terminals.